## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of;

Young-woo PARK, et al.

Group Art Unit: Unassigned

Application Serial No. Unassigned

Examiner: Unassigned

Filed: January 16, 1998

Title: SEMICONDUCTOR MEMORY DEVICE AND METHOD OF

MANUFACTURING FOR PREVENTING BIT LINE OXIDATION

**Assistant Commissioner of Patents** 

Washington, D.C. 20231

## INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is being filed in accordance with 37 C.F.R. §§ 1.56, 1.97(b), & 1.98. It is being filed concurrently with the filing of the application referenced above. The cited document is listed in an attached form PTO-1449. A copy of the listed document is also attached.

Applicant respectfully requests that the Examiner consider the attached document and indicate this consideration by initialing the attached form PTO-1449.

This Information Disclosure Statement is not to be considered an admission that the listed document is prior art in the application referenced above. Applicant reserves the right to offer proof that the listed document may not be considered valid prior art in the above-referenced application.

Respectfully Submitted,

William L. Geary, Jr.

Reg. No. 35,879

Date: January 16, 1998

Samsung Electronics Co., Ltd. Suite 550 1200 New Hampshire Ave. N.W. Washington, D.C. 20036

Tel: (202) 887-5671 Fax: (202) 331-7961

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